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Subject: Re: The power of Makefile

Posted by [masu](#) on Mon, 15 Feb 2010 15:24:58 GMT

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Hi Honza,

first, thanks for your appreciation .

A problem I have is to find a way to define variables when calling make which can be extended then during the make flow.

I thought `VAR+=x` should work, but it does not work for env variables defined as parameter to make, like this:

```
make DEFS=-DflagNOGTK
```

In this case the variable DEFS is completely overwritten by `-DflagNOGTK`. But what I want is to add this flag to already defined flags inside the Makefile.

Do you have an idea?

Matthias

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