

---

Subject: Re: NTL and C4251

Posted by [mirek](#) on Tue, 25 Apr 2006 12:20:42 GMT

[View Forum Message](#) <> [Reply to Message](#)

---

jmansion wrote on Tue, 25 April 2006 08:07I would still look to a generator for the bits that can be easily automated

Actually, I never got that part about "easily automated". With well designed C++ code, it is IME as easy to write descent parser right away as to create syntax description for "code generator" (or even to describe grammar in any formal way).

BTW, when developing Esc interpreter, I have never had any formal grammar for the language - descent parser C++ code is as good as the formal grammar description.

Quote:  
AST  
CoCo/R

What are these?

Quote:  
sane and the generators won't help you here, but using LLVM as the back end surely will.

Yes, I agree about using LVM (if that is what I think: "platform independent assembler-like language"). Yet another option is to produce plain C....

Mirek

---