
Subject: Re: SSE2(/AVX) and alignment issues
Posted by [cbpporter](#) on Fri, 28 Jan 2011 12:55:51 GMT
[View Forum Message](#) <> [Reply to Message](#)

How about an optional flag for option number 1. Compile with alignment if memory is not an issue for you.

for option number two, can't we have VectorSSE2 variant living in parallel with normal one?
