Subject: Re: Rapsberry PI - cpu dilemma

Posted by Novo on Sat, 09 May 2020 05:08:09 GMT

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amrein wrote on Sat, 09 May 2020 00:29

We don't do this for amd, amd rizen of intel, so why bother?

Do you mean why somebody wants to compile code without exception support?

For the same reason why Sony removed exception support from Clang for their PlayStation 4, which is based on amd rizen.

Exception support is a complicated thing, especially on 32-bit platforms.

World of small devices is very different from regular PCs. People do care about instruction cache misses there.