
Subject: SIMD encapsulation

Posted by [mirek](#) on Fri, 03 Jul 2020 08:17:47 GMT

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While thinking about optimizing for ARM NEON SIMD, after a lot hesitation I decided to create "common encapsulation", basically a set of SIMD types common for SSE2 and ARM NEON and rewrote all SIMD optimised algorithms with it. It is in Core/SIMD_SSE2.h (I yet have to create NEON implementation).

While mostly intended for internal use, it could make developing SIMD code easier. It basically defines 4 128bit types and operations on them...

Mirek

Subject: Re: SIMD encapsulation

Posted by [mirek](#) on Tue, 28 Jul 2020 11:32:01 GMT

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ARM NEON implementation finished... Results are not spectacular, but at least it seems faster than non-SIMD (by about 20%).
